

	Type	L #	Hits	Search Text	DBs	Time Stamp
1	IS&R	L1	1140	(712/234,233,237,24,26).CCLS.	US- PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	2005/10/30 14:57
2	BRS	L2	1164	receive with process with cycle	US- PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	2005/10/30 15:03
3	BRS	L3	3	(receive near3 branch adj instruction) with process	US- PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	2005/10/30 15:03
4	BRS	L4	0	(receive near3 branch adj instruction) with process with clock adj cycle	US- PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	2005/10/30 15:06
5	BRS	L5	0	3 and clock adj cycle	US- PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	2005/10/30 15:04
6	BRS	L6	0	3 and cycle	US- PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	2005/10/30 15:04

7	BRS	L7	82	(receive near3 branch adj instruction) and clock adj cycle	US- PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	2005/10/30 15:06
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	Type	L #	Hits	Search Text	DBs	Time Stamp
8	BRS	L8	2	(receive near3 branch adj instruction) with clock adj cycle	US- PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	2005/10/30 15:09
9	BRS	L9	0	(receive near3 branch adj instruction) with single adj cycle	US- PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	2005/10/30 15:09
10	BRS	L10	8	(single adj cycle near3 branch adj instruction)	US- PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	2005/10/30 15:10
11	BRS	L11	8	10 not 8	US- PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	2005/10/30 15:10